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Abstract A system chip and related method of data access are provided. The system chip encompasses a central processing unit and a control circuit of interface. The control circuit of interface is configured for transforming an internal access address of data into one corresponded to an external temporary segment of data, so that the CPU may directly access data with the external temporary region of data. The method of data access includes the steps of determining whether an access address of data is corresponded to an internal memory, transforming the access address into one corresponded to an external memory, and requesting data access with the external memory. The data access may be suspended prior to sending CPU : confirmation of request while the data access may be executed with the external memory when the request is confirmed by the external memory.

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